

## DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention CAPACITIVE COUPLING REDUCTION USING VOID FORMATION FOR USE WITH A SEMICONDUCTOR DEVICE, the specification of which:

X is attached hereto.

\_\_\_\_\_ was filed on \_\_\_\_\_, as Application Serial No. \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims.

I acknowledge the duty to disclose information which is material to patentability of the subject matter claimed in this application as "materiality" is defined in Title 37 of the Code of Federal Regulations, § 1.56.

I hereby claim the benefit of any earlier filing date in the United States to which I am entitled under Title 35 of the United States Code, § 120 and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 of the United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 of the Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

\_\_\_\_\_  
(Application Serial No.)

\_\_\_\_\_  
(Filing Date)

\_\_\_\_\_  
(Status)

Send correspondence to:

Kevin D. Martin, Mail Stop 525  
Micron Technology, Inc.  
8000 S. Federal Way  
Boise, Idaho 83706  
(208) 368-4516

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18 of the United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first or sole inventor Philip J. Ireland

Inventor's Signature: Philip J. Ireland  
(First, Middle Initial, Last)

Date: 2/6/02

Residence Address: 6722 E LOCUST LN

City, State, Country: NAMPA, ID United States of America

Citizenship: USA

Post Office Address: Same as residence address

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Philip J. Ireland  
Serial No.: Unknown  
Filed: Concurrently Herewith (February 6, 7<sup>th</sup> 2002)  
For: CAPACITIVE COUPLING  
REDUCTION USING VOID FORMATION  
FOR USE WITH A SEMICONDUCTOR  
DEVICE

§ Atty. Docket: 2000-0995.00US  
§  
§ "EXPRESS MAIL" MAILING LABEL NUMBER: EV009987419US  
§ DATE OF DEPOSIT: February 7, 2002  
§ I HEREBY CERTIFY THAT THIS PAPER IS BEING DEPOSITED WITH  
§ THE UNITED STATES POSTAL SERVICE "EXPRESS" MAIL POST  
§ OFFICE TO ADDRESSEE" SERVICE UNDER 37 C.F.R. § 1.10 ON THE  
§ DATE INDICATED ABOVE AND IS ADDRESSED TO THE  
§ COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.  
§  
§ *Peggy Lloyd-Foster*  
§ Signature

ELECTION UNDER 37 C.F.R. §§ 3.71 AND 3.73 AND POWER OF ATTORNEY

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

The undersigned, being Assignee of the entire interest in the above-identified application by virtue of an Assignment recorded in the United States Patent and Trademark Office as set forth below or filed herewith, hereby elects, under 37 C.F.R. § 3.71, to prosecute the application to the exclusion of the inventor(s).

The Assignee hereby revokes any previous Powers of Attorney and appoints: Michael L. Lynch, Reg. No. 30,871; Charles B. Brantley, II, Reg. No. 38,086; Kevin D. Martin, Reg. No. 37,882; and David J. Paul, Reg. No. 34,692 as its attorney or agent, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Pursuant to 37 C.F.R. § 3.73, the undersigned duly authorized designee of Assignee certifies that the evidentiary documents have been reviewed, specifically the Assignment to MICRON TECHNOLOGY, INC., referenced below, and certifies that to the best of my knowledge and belief, title remains in the name of the Assignee.

Assignment:

☒ Filed concurrently herewith for recording, a copy of which is attached hereto.

\_\_\_ Previously recorded on: \_\_\_\_\_  
at Reel: \_\_\_\_\_ Frame: \_\_\_\_\_

Please direct all communications as follows:

Kevin D. Martin, Mail Stop 525  
MICRON TECHNOLOGY, INC.  
8000 S. Federal Way  
Boise, ID 83706-9632  
(208) 368-4516

Date: 2/6/02 <sup>x</sup>

ASSIGNEE: MICRON TECHNOLOGY, INC.

By: Roderic W. Lewis  
Roderic W. Lewis, Vice President-Legal Affairs  
General Counsel and Corporate Secretary